

## EUROPEAN PATENT OFFICE

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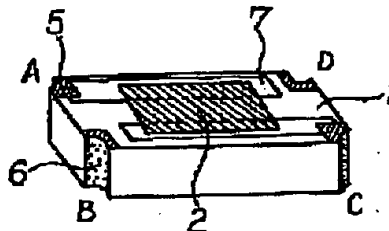
APPLICATION DATE : 09-09-92  
APPLICATION NUMBER : 04240452

APPLICANT : NEC CORP;

INVENTOR : ISHIHAMA KAZUHARU;

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TITLE : CHIP TYPE RESISTOR



ABSTRACT : PURPOSE: To improve the mounting density of each chip type resistor, speed up the adjustment of a resistance value, and improve the precision of a resistor.

CONSTITUTION: Resistors 2 are arranged and formed on the surface and the rear of a ceramic substrate 1, which are connected by using an electrode formed at a specified position of the ceramic substrate 1. Cutting process is performed in order to obtain a desired resultant resistance value when at least either one of the resistors 2 is connected in series or in parallel. In this case, it is preferable that the mounting rear side is subjected to cutting adjustment and the surface side is left in the state without cutting, for further adjustment after mounting.

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